



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

of 4

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187 2189
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2 (if known)}			
CPC		US-5742840	04-21-1998	Hansen et al.	
CPC		US-5748872	05-05-1998	Norman	
CPC		US-5860080	01-12-1999	James et al.	
CPC		US-5867422	02-02-1999	John	
CPC		US-5898863	04-27-1999	Ofer et al.	
CPC		US-6006318	12-21-1999	Hansen et al.	
CPC		US-6034878	03-07-2000	Osaka et al.	
CPC		US-6038682	03-14-2000	Norman	
CPC		US-6092229	07-18-2000	Boyle et al.	
CPC		US-6125419	09-26-2000	Umemura et al.	
CPC		US-6151648	11-21-2000	Haq	
CPC		US-6154826	11-28-2000	Wulf et al.	
CPC		US-6160423	12-12-2000	Haq	
CPC		US-6255859	07-03-2001	Haq	
CPC		US-6263413	07-17-2001	Motomura et al.	
CPC		US-6327205	12-04-2001	Haq	
CPC		US-6408402	06-18-2002	Norman	
CPC		US-6487102	11-26-2002	Halbert et al.	

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Ant P. C

Date Considered

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Sheet 3

of

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Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CPC		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt	
CPC		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt	
CPC		NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39	
CPC		SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42	

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Considered

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*Please note that the copies of these references enclosed herewith were obtained from the file history for U.S. Patent Application Serial No. 10/456,353. Applicants' attorney assumes that they are the correct copies.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/699,116
		Filing Date	October 31, 2003
		First Named Inventor	Donnelly
		Art Unit	2487 2189
		Examiner Name	Chace, C.
Sheet 4 of 4	Attorney Docket Number	RAMB-01065US1	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CPC		JONES, F (United Memories, Inc.), "A new era of fast dynamic RAMs", IEEE Spectrum, October 1992, pp. 43-49	
CPC		FARMWALD, M. et al. (Rambus Inc.), "A fast path to one memory", IEEE Spectrum, October 1992, pp. 50-51	
CPC		GJESSING, S. (University of Oslo), GUSTAVSON, D.B (Stanford Linear Accelerator Center), JAMES, D. et al. (Apple Computer Inc.), WIGGER, H. (Hewlett-Packard Co.), "A RAM link for high speed", IEEE Spectrum, October 1992, pp. 52-53	
CPC		The Institute of Electrical and Electronics Engineering, Inc., "IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)", 1996, pp. 1-91	

Examiner Signature	<i>C. A. P. C.</i>	Date Considered	7/5/05
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Application Number	10/699,116
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Art Unit	2487 2169
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

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CPC		US-6016282	1-18-2000	Keeth	
CPC		US-6449727	09-10-2002	Toda	
CPC		US-4644532	02-17-1987	George et al.	
CPC		US-4667305	05-19-1987	Dill et al.	
CPC		US-4747100	05-24-1988	Roach et al.	
CPC		US-4864563	09-05-1989	Pavey et al.	
CPC		US-5228134	07-13-1993	MacWilliams et al.	
CPC		US-5355467	10-11-1994	MacWilliams et al.	
CPC		US-5408646	04-18-1995	Olnowich et al.	
CPC		US-5537394	07-16-1996	Abe et al.	
CPC		US-5544342	08-06-1996	Dean	
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CPC		US-5557266	09-17-1996	Calvignac et al.	
CPC		US-5604735	02-18-1997	Levinson et al.	
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CPC		US-5659710	08-19-1997	Sherman et al.	
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CPC		US-5802054	09-01-1998	Bellenger	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
CPC		WO 99/30240	06-17-1999	Intel Corporation		
CPC		WO 99/41666	08-19-1999	Intel Corporation		
CPC		WO 99/41667	08-19-1999	Intel Corporation		
CPC		EP 1069509 A2	01-17-2001	Texas Instruments Incorporated		

Examiner Signature	<i>Ant P. Le</i>	Date Considered	7/5/05
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Sheet 2

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Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2487 2189
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

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		Number-Kind Code ² (if known)			
CPC		US-5802565	09-01-1998	McBride	
CPC		US-5805798	09-08-1998	Kearns et al.	
CPC		US-5848145	12-08-1998	Gallagher et al.	
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CPC		US-5953215	09-14-1999	Karabatsos et al.	
CPC		US-6142830	11-07-2000	Looeffler	
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CPC		US-6446158	09-03-2002	Karabatsos	

FOREIGN PATENT DOCUMENTS

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CPC		US-6493250	12-10-2002	Halbert et al.	
CPC		US-6530006	03-04-2003	Dodd et al.	
CPC		US-6449679	09-10-2002	Ryan et al.	
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CPC		US-5701313	12-23-1997	Purdham	
CPC		US-5513135	04-30-1996	Dell et al.	
CPC		US-6065092	05-16-2000	Roy	
CPC		US-5034917	07-23-1991	Bland et al.	
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CPC		US-6690726	02-10-2004	Yavits et al.	
CPC		US-5325493	06-28-1994	Herrell et al.	
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CPC		US-6104417	08-15-2000	Nielsen et al.	
CPC		US-6208273	03-27-2001	Dye et al.	
CPC		US-5883839	03-16-1999	Tosaka et al.	

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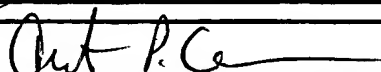
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OPC		US-20040260991	12-23-2004	Vogt et al.	
OPC		US-20040250181	12-09-2004	Vogt et al.	
OPC		US-20040250153	12-09-2004	Vogt	
OPC		US-20040150024	12-09-2004	Vogt	
OPC		US-20040246786	12-09-2004	Vogt	
OPC		US-20040246785	12-09-2004	Vogt	
OPC		US-20040246767	12-09-2004	Vogt	
OPC		US-20040015650	01-22-2004	Zumkehr et al.	
OPC		US-20030177313	09-18-2003	Iyer et al.	
OPC		US-20020083287	06-27-2002	Zumkehr et al.	
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CPC		Intel Developer Forum, "DDR2 Memory In 2004 Servers - Recipes For Successful Designs", September 2003	
CPC		QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
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Examiner Signature		Date Considered	7/15/05
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 6

of

7

Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187 2189
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CPC		GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SDRAM Consortium, SDRAM Inc., pp. 1-14 (Aug. 1997)	
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CPC		POULTON, J., "Signaling in High Performance Memory Systems", IEEE Solid State Circuits Conference, slides 1-59 (Feb. 1999)	
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Examiner
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Considered

7/5/05

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		Filing Date	October 31, 2003		
		First Named Inventor	Donnelly		
		Art Unit	2187 2189		
		Examiner Name	Chace, C.		
Sheet	7	of	7	Attorney Docket Number	RAMB-01065US1

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ARC		IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
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CRC		JEDDELOH, JOE, "Fully Buffered DIMM (FB-DIMM)", Advanced Systems Technology, Micron Technology, Inc. April 15-16, 2004	

Examiner Signature	<i>Art P. C.</i>	Date Considered	7/5/05
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